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APPLICANTS Tomio Matsuzaki, Tokyo, JAPAN; Kazuyoshi Arai, Tokyo, JAPAN;					
** CONTINUING DATA ***** <i>none Loke</i>					
** FOREIGN APPLICATIONS ***** JAPAN 2002-324973 11/08/2002 JAPAN 2003-147447 05/26/2003 JAPAN 2003-324204 09/17/2003 <i>Loke</i>					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/06/2004					
Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <i>Stev Loke</i> Examiner's Signature	STATE OR COUNTRY JAPAN	SHEETS DRAWING 56	TOTAL CLAIMS 35	INDEPENDENT CLAIMS 2
ADDRESS 01933 FRISHAUF, HOLTZ, GOODMAN & CHICK, PC 767 THIRD AVENUE 25TH FLOOR NEW YORK , NY 10017-2023					
TITLE Wiring structure on semiconductor substrate and method of fabricating the same					
FILING FEE RECEIVED 1040	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other			